

REMARKS

Claims 1-3, 5 and 7-11 are currently pending. Claims 4 and 6 were previously canceled. By this amendment, Applicants have amended all pending claims. In the most recent Office Action (Advisory action mailed November 24, 2008), the Examiner maintained the prior rejection of claims 1-3, 5 and 7-11 as anticipated by US Patent No. 5,182,810 to Bartling et al. ("Bartling"). Applicants submit that the claims as currently amended are not anticipated by Bartling inasmuch as Bartling fails to disclose each and every element of the pending claims.

In again rejecting claims 1-3, 5 and 7-11 as anticipated by Bartling, the Examiner asserts:

In view of the fact, that **BARTLING** teaches, "the ancillary chip 110 receives ...the output VCCO1 powers the nonvolatile microcontroller, and the output VCCO2 powers the liquid-crystal display driver 140" (Bartling, Col. 8:48-52). Thus, it is evidently, the explanations above is directed to telecommunications systems and methods for in a low-power microprocessor system where a communication link interfaced to the microprocessor via using buses (as well-known in the art), therefore **the above communication lines feeding signals via data-buses** as shown in the cited section, that positively, anticipated by **BARTLING**. Hence, it is believed that **BARTLING** still teaches the claimed inventions.

Advisory Action at 2 (emphases in original).

From the above, the Examiner concedes that VCCO1 and VCCO2 are not data buses. In fact, how could they be? Bartling makes abundantly clear that VCCO1 and VCCO2, as well as VCCO3 are for supplying power to the microcontroller, LCD drive and LED, respectively. Having conceded that VCCO1 and VCCO2 are not data buses, the Examiner now argues that because Bartling is directed to telecommunication systems using a low-power microprocessor system Bartling must necessarily teach communication lines feeding signals via data buses. Applicants disagree with the Examiner's conclusion based on the absence of an

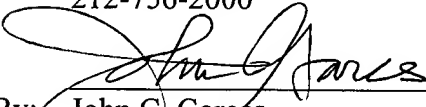
express teaching in Bartling. However, even assuming *arguendo* the Examiner's argument to be correct, Bartling does not disclose, expressly or inherently, the claimed "mobile telephone." As the Examiner is aware for a reference to anticipate a pending claim the anticipating reference must disclose each and every element of the allegedly anticipated claim. Here, the Examiner only argues that Bartling must disclose data buses but does not establish that Bartling discloses "a first bus for connecting to controller and memory means that are provided as principal integrated circuits; and a second bus for connecting said controller and said display; wherein said first bus and said second bus are independently connected to said controller, wherein said first bus is not connected to said display, and said second bus is not connected to said memory; and wherein said controller controls said first bus and said second bus independently," as required by independent claim 1. Similarly, the Examiner has not established that Bartling discloses "a first bus for connecting to controller and memory; a second bus for connecting said controller and said display; wherein said first bus and said second bus are independently connected to said controller, wherein the first bus is not connected to said display, and said second bus is not connected to said memory," as required by independent claim 7. Failing to disclose the first and second buses of claims 1 and 7, Bartling cannot anticipate those claims. For the same reasons, all claims dependent on claims 1 and 7 cannot be anticipated by Bartling. Accordingly, claims 1, 3, 5 and 7-11 are not anticipated by Bartling.

Applicants respectfully submit that this patent application, as amended, is in condition for allowance. Reconsideration and prompt allowance of this application are respectfully requested. The Examiner is urged to telephone Applicants' undersigned counsel at the number noted below if it will advance the prosecution of this application, or with any suggestion to resolve any condition that would impede allowance. In the event that any extension of time is required, Applicants petition for that extension of time required to make this

response timely. Kindly charge any additional fee, or credit any surplus, to Deposit Account No. 50-0675, Order No. 848075-0016.

Respectfully submitted,

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New York, New York


CERTIFICATE OF MAILING

Date of Deposit: December 23, 2008

I hereby certify under 37 C.F.R. 1.8 that this correspondence and enumerated documents are being deposited with the United States Postal Service as Express Mail EV 320048938 US with sufficient postage on the date indicated above and is addressed to Mail Stop RCE, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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